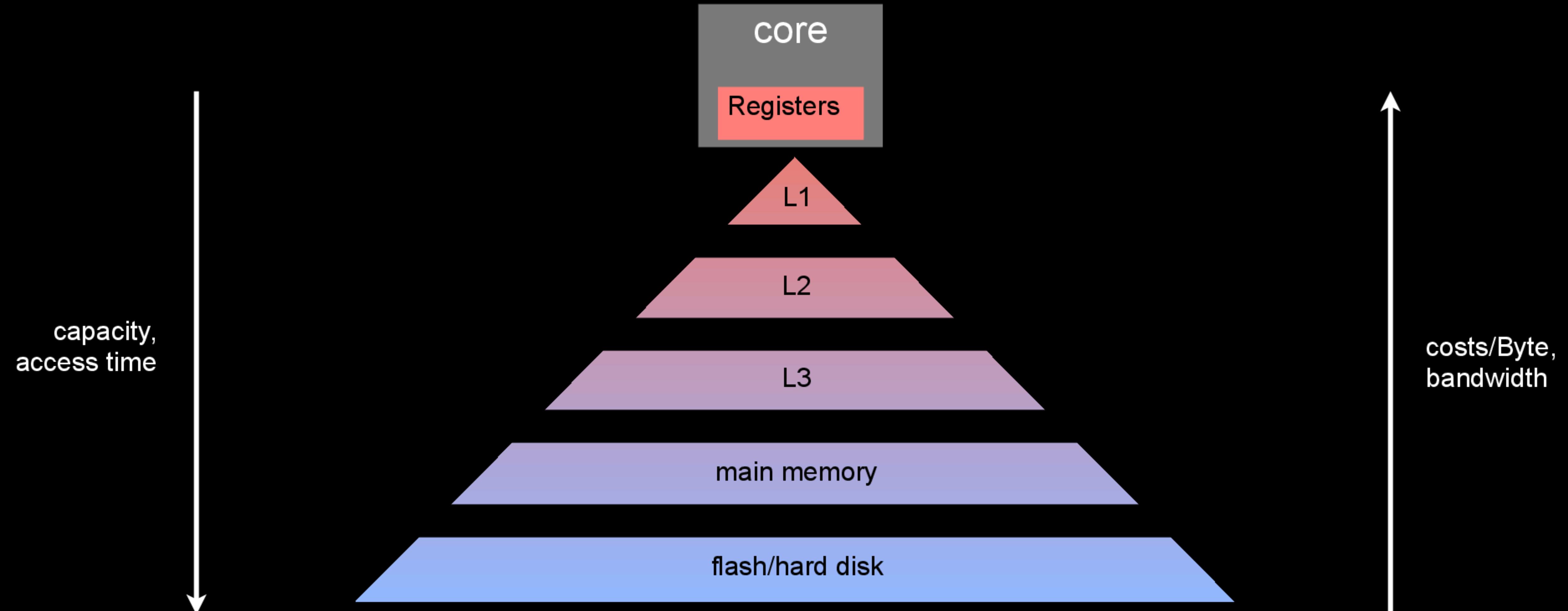
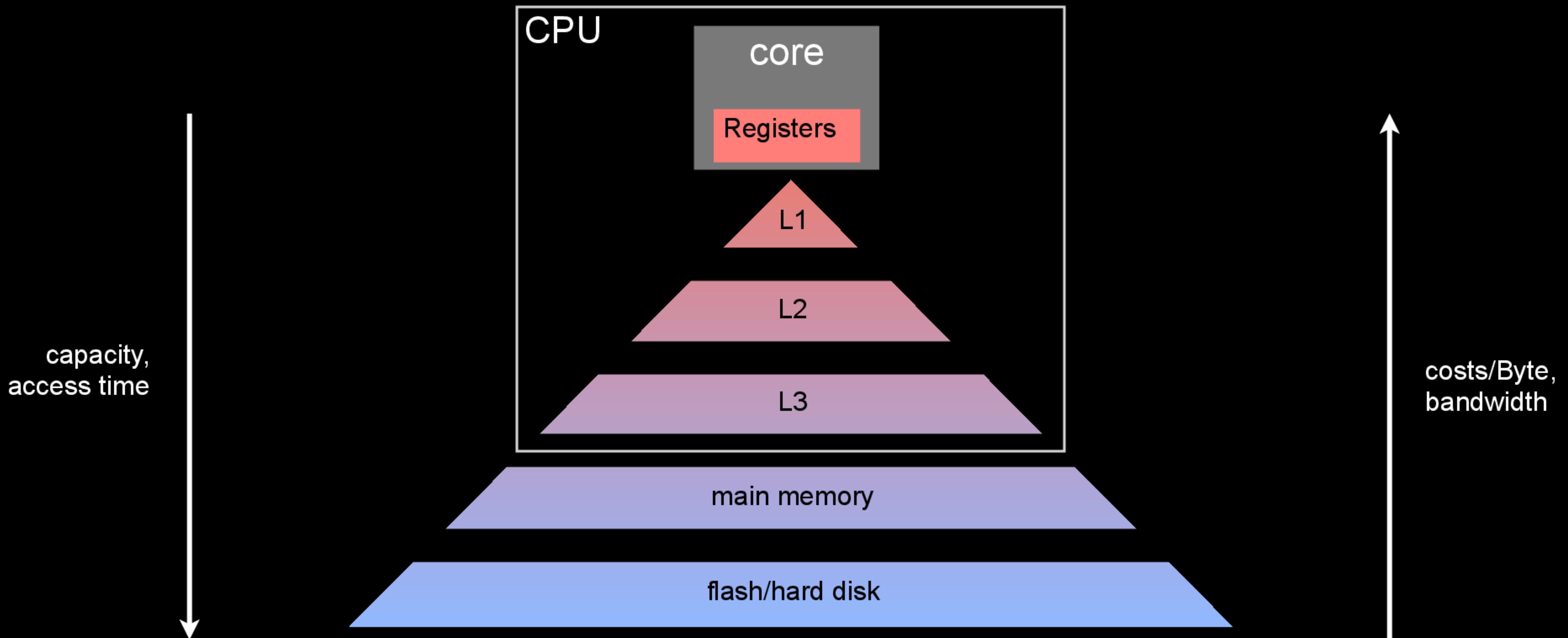


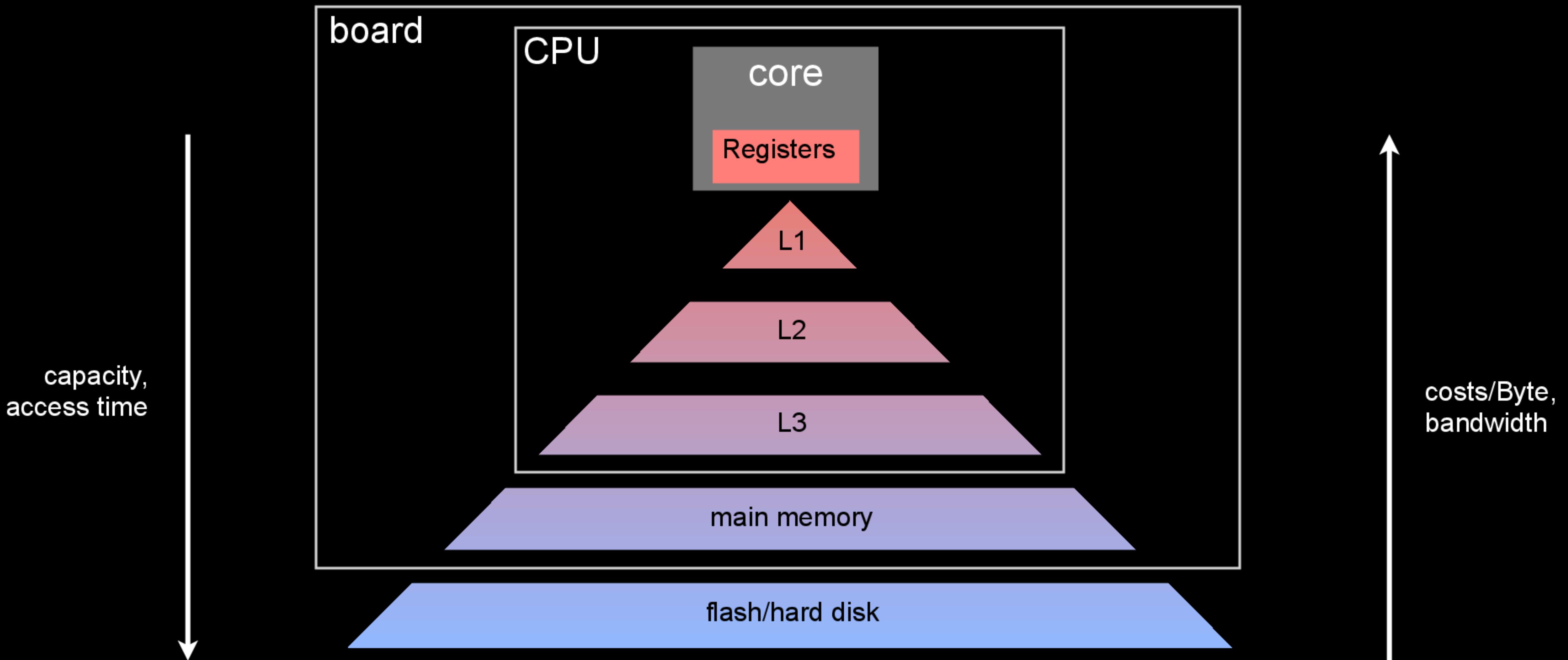
A Single-Core Storage Hierarchy



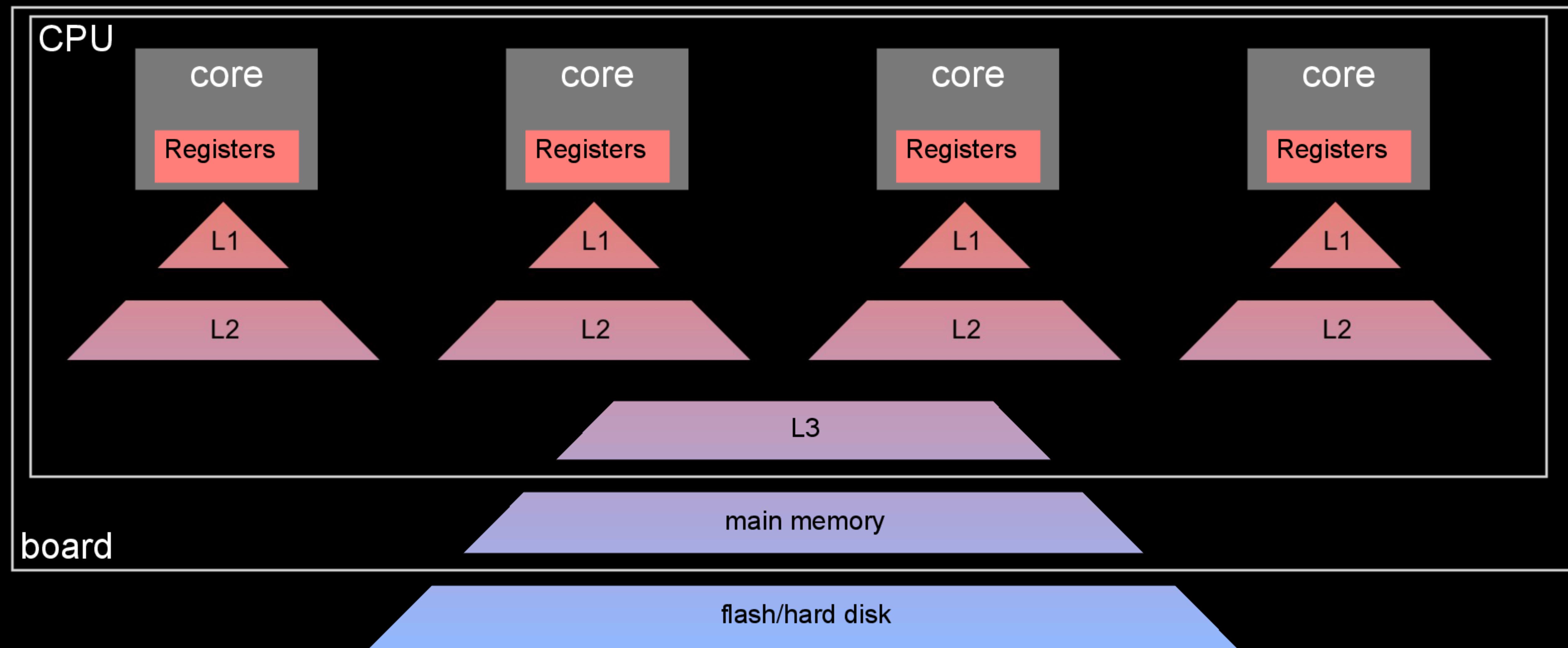
A Single-Core Storage Hierarchy



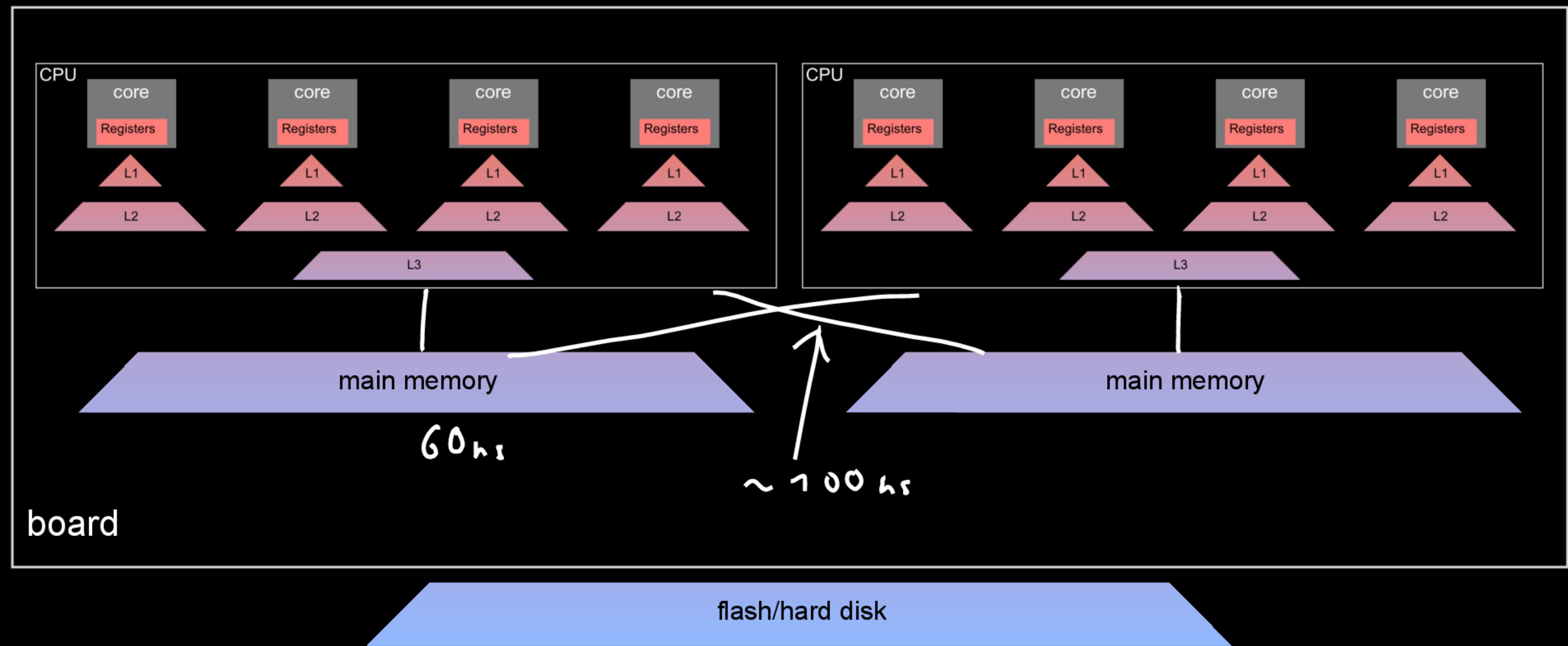
A Single-Core Storage Hierarchy



A Multicore Storage Hierarchy



Non-Uniform Memory Access (NUMA)



Credits

Cache latency numbers are based on this article:

Performance Analysis Guide for Intel® Core™ i7 Processor and Intel® Xeon™ 5500 processors

By Dr David Levinthal PhD. Version 1.0

http://software.intel.com/sites/products/collateral/hpc/vtune/performance_analysis_guide.pdf

[retrieved Nov 8, 2013]